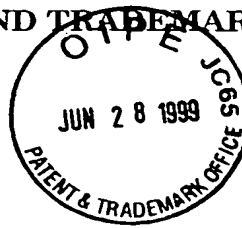


P1500.D1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 5,903,260
Issued: May 11, 1999
Name of Patentee: Youichi Imamura
Serial No.: 08/582,771
Filing Date: January 02, 1996
Title of Invention: Flat Panel Device And Display Driver With On/Off Power Controller Used To Prevent Damage To The LCD (As Amended)



CERTIFICATE OF MAILING

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee" service and is addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on this date.

Date: June 28, 1999
"Express Mail" Mailing Label Number: EL034498797US

Shellie Bailey
Shellie Bailey

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR PTO MISTAKE (37 CFR 1.322(a))

Assistant Commissioner for Patents
Washington, D.C. 20231

ATTENTION: Decision and Certificate of Correction Branch of the Patent Issue Division

Sir:

It is noted that errors appear in this patent of a clerical, typographical or minor nature or character, as more fully described below, due to a mistake by the Patent and Trademark Office.

The original U.S. Letters Patent is enclosed for reprinting. Also attached hereto in duplicate is Form PTO-1050 with at least one copy being suitable for printing.

The exact page/column and item/line numbers where the errors occur in the patent are:

Title page, item 54, Title: Change "Flat Device" to --Flat Panel Device--.

Column 15, line 10, delete ",", after "voltage".

APPROVED
OCT 1 1999

FOR THE COMMISSIONER OF PAT. & T.M.

REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PTO MISTAKE (37 CFR 1.322(a))

Patentee's undersigned attorney may be reached at the telephone number listed below. All correspondence should continue to be directed to our below listed address.

Respectfully submitted,



Eric B. Janofsky
Attorney for Patentee
Registration No. 30,759

Please address all correspondence to:
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Date: June 28, 1999

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 5,903,260
DATED : May 11, 1999
INVENTOR(S) : Youichi Imamura

It is certified that errors appear in the above identified patent and that said Letters Patent is hereby corrected as shown below:

- ✓ Title page, item 54, Title: Change "Flat Device" to --Flat Panel Device-- ✓
✓ Column 15, line 10, delete ",", after "voltage". ✓

MAILING ADDRESS OF SENDER:

Epson Research and Development, Inc.
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San Jose, CA 95134
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PATENT NO. 5,903,260

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